

IN THE CLAIMS

1. (Currently amended) A regulating system for regulating, with respect to a reference level (V_{ref}), the level of an amplified signal (408), said regulating system comprising: ~~_____~~ ~~attenuation means circuitry (201)~~ for generating an attenuated signal (202) from said amplified signal (408) according to a programmable attenuation factor, and ~~_____~~ ~~conversion means circuitry (203)~~ for converting said attenuated signal (202) in order to generate an output signal (204) ~~intended to be compared~~ for comparison with said reference level (V_{ref}), wherein said conversion circuitry generates said output signal with a level proportional to the square of the effective value of said attenuated signal.

2. (Currently amended) A regulating system as claimed in claim 1, wherein: said ~~attenuation means (201)~~ comprise a network of resistances (~~R_{p1} , R_{s1} , R_{p2} , R_{s2} , R_{p3} , R_{s3} , R_{p4} , R_{s4} , R_{p5} , R_{s5}~~) defined by a set of π -structures connected in series, each node (~~A, B, C, D, E, F~~) of the π -structures being connected to a switch (~~SWA, SWB, SWC, SWD, SWE, SWF~~) intended to be activated for defining said programmable attenuation factor, ~~said conversion means (203) comprise processing means (401, 402) for generating said output signal (204) with a level proportional to the square of the effective value of said attenuated signal (202).~~

3. (Currently amended) A regulating system as claimed in claim 2, wherein the switches (~~SWA, SWB, SWC, SWD, SWE, SWF~~) are intended to be activated by a command word (~~SA, SB, SC, SD, SE, SF~~) delivered by a digital bus (~~301~~).

4. (Currently amended) A regulating system as claimed in ~~one of claims 1 to 3~~claim 1, comprising a voltage comparator ~~(205)~~ including an adjustable voltage/current converter ~~(403)~~, for generating an output current signal I_{ACC} being proportional to the difference between said output signal ~~(204)~~ and said reference level ~~(V_{ref})~~.

5. (Currently amended) An integrated circuit comprising a regulating system as claimed in ~~one of claims 1 to 4~~claim 1.

6. (Currently amended) A tuner comprising a regulating system as claimed in ~~one of claims 1 to 4~~claim 1.